In Re Application of Kelvin S. Vartti et. al. Title: Programmable System & Method for Accessing a Shared Memory File #5487 – Customer # 27516 Attorney: Beth L. McMahon, Reg. 41, 987, 651-635-7893 1 of 6 Drawings PROCESSING NODE ₾ ₫ 120B  $S_{LC}$ SLC SHARED CACHE 104B 8 8 LOGIC 8 8 SLC SLC ٥. ₫ 30 ᡆ <u>a</u>

H108~

PROCESSOR NODE ·109B DIRECTOR SCAN PROCESSING NODE -102A 1120 SLC SLC PROCESSOR NODE SHARED CACHE 109A -101 104A ပ္ထ 8 DIRECTOR LOGIC DIRECTORY 8 8 SLC SLC 1, L ₫. <u>a</u> 110A~

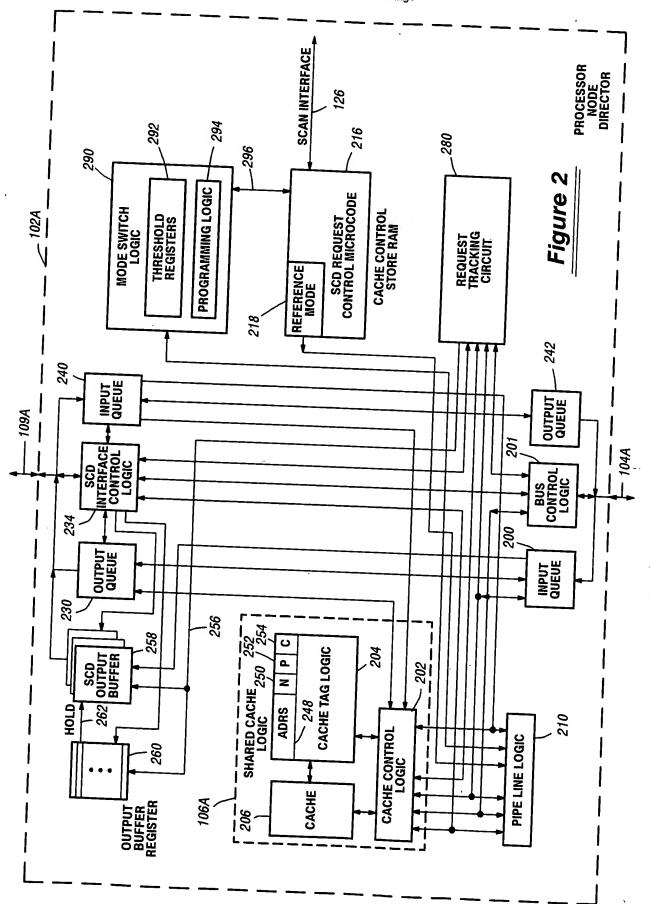
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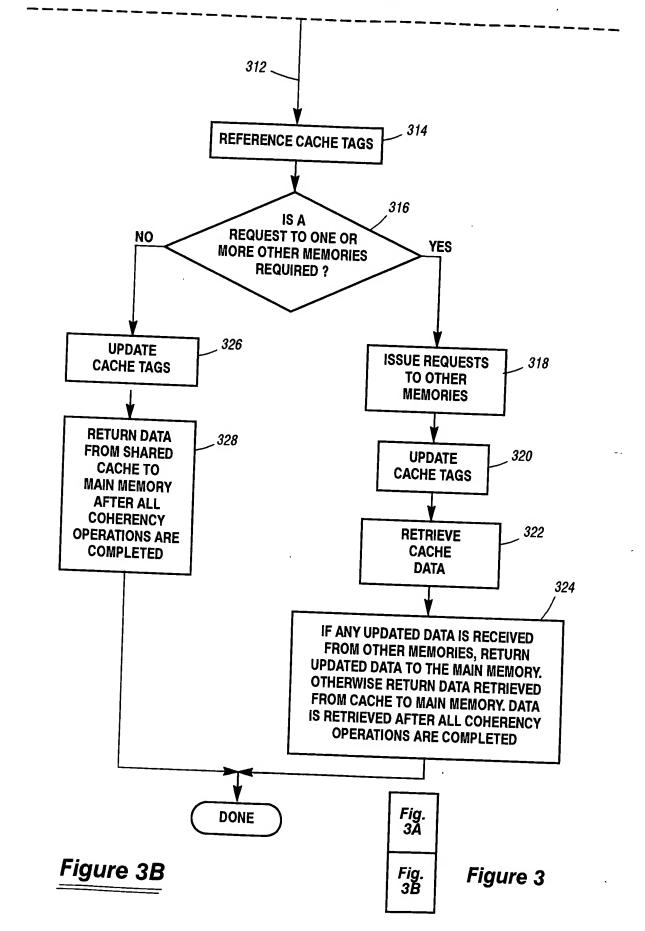
Memory File #5487 - Customer # 27516 Attorney: Beth L. McMahon, Reg. 41, 987, 651-635-7893 3 of 6 Drawings 300 REQUEST FOR DATA IS. **RECEIVED FROM MAIN MEMORY** 302 IS THE REFERENCE INDICATOR **YES** SET TO "CACHE NOT **PRESENT"** MODE? NO 304 310 **ISSUE REQUEST** IS THE TO ONE OR MORE REFERENCE INDICATOR SET YES OTHER MEMORIES TO "SHARED CACHE" MODE? 330 ISSUE REQUEST TO ONE OR **MORE OTHER MEMORIES** 306 IF ANY UPDATED 332 **UPDATE CACHE TAGS DATA IS RECEIVED** FROM OTHER MEMORIES, **RETURN UPDATED DATA TO MAIN MEMORY** 334 RETRIEVE CACHE DATA IF ANY UPDATED DATA IS RECEIVED FROM OTHER MEMORIES, RETURN 336 UPDATED DATA TO MAIN MEMORY, OTHERWISE RETURN DATA RETRIEVED FROM CACHE TO MAIN MEMORY. DATA IS RETURNED AFTER ALL COHERENCY **OPERATIONS ARE COMPLETED** 312. Fig. 3A DONE Figure 3A Fig. 3B Figure 3

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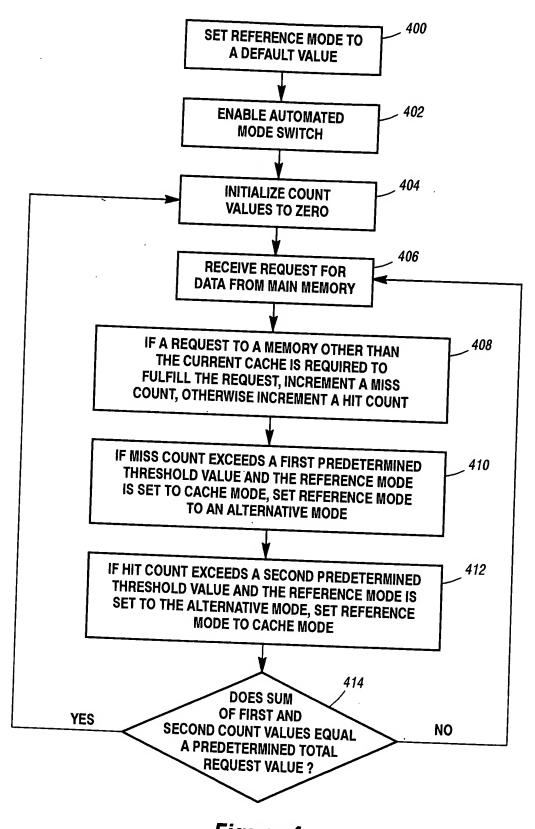


Figure 4

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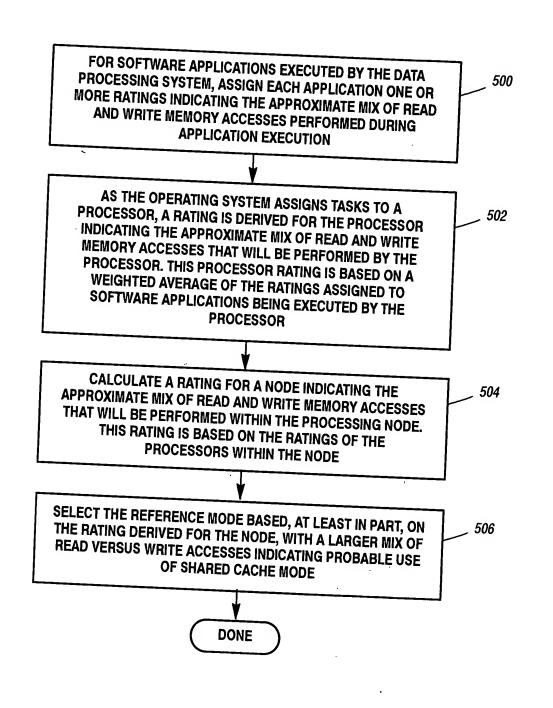


Figure 5